

Notice of References Cited

Application/Control No.

09/676,424

Applicant(s)/Patent Under
Reexamination
WEGMAN ET AL.

Examiner

LeChi Truong

Art Unit

2126

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,926,632	07-1999	Kawaguchi, Kenichi	716/7
	B	US-6,437,804 B1	08-2002	Ibe et al.	345/736
	C	US-6,253,372	06-2001	Komatsu et al.	717/149
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Blainey Robert James, Loop Allocation for optimizing compilers, 08/11/1999.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.